



<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		<b>Complete if Known</b>			
		Application Number	09/930,836		
		Filing Date	August 15, 2001		
		First Named Inventor	Paul C. Kocher		
		Group Art Unit	2132		
		Examiner Name	Virgil A. Herring		
Sheet	1	of	1	Attorney Docket No.	44424162-8724
<b>OTHER ITEMS - NON PATENT LITERATURE DOCUMENTS</b>					
Examiner Initials*	CiteNo.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T <sup>2</sup>
NH/	V53	SCHNEIER, Bruce, <u>Applied Cryptography</u> , Chapter 12, pp. 265-301, John Wiley & Sons, Inc. (2ndEd. 1996), New York, NY.			
Examiner Signature	/Virgil Herring/ (05/15/2008)			Date Considered	

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U.S. PATENT DOCUMENTS					
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	V54	US - 4,309,569	01-05-1982	Merkle	
	V55	US - 5,434,919	07-18-1995	Chaum	

U.S. PATENT DOCUMENTS PURSUANT TO McKESSON (notices of allowance)					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Notices of Allowance mailed:
	8712	US 20030028771 A1	02-06-2003	Paul C. Kocher et al.	12-07-2007

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	0097	US 2006/0045264 A1	03-02-2006	Paul C. Kocher et al.	11/252,898 03-02-2006
	0140	US 2008/0022146 A1	01-24-2008	Paul C. Kocher et al.	11/643,349 12-21-2006
	0167	USSN 11/977,392		Paul C. Kocher et al.	11/977,392 1/24/2007
	0168	USSN 11/981,495		Paul C. Kocher et al.	11/981,495 10/30/2007
	0169	USSN 11/978,364		Paul C. Kocher et al.	11/978,364 10/29/2007
	8710	US 6,304,658 B1	10-16-2001	Paul C. Kocher et al.	
	8711	US 6,381,699 B1	04-30-2002	Paul C. Kocher et al.	
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	8712	US 2003/0028771 A1	02-06-2003	Paul C. Kocher et al.	10/136,012 04-29-2002
	8718	US 6,298,442 B1	10-02-2001	Paul C. Kocher et al.	
	8720	US 6,327,661 B1	12-04-2001	Paul C. Kocher et al.	
	8721	US 2002/0124178 A1	09-05-2002	Paul C. Kocher et al.	10/005,105 12-03-2001
	8723	US 6,278,783 B1	08-21-2001	Paul C. Kocher et al.	
	8726	US 6,539,092 B1	03-25-2003	Paul C. Kocher et al.	
	8727	US 2003/0188158 A1	10-02-2003	Paul C. Kocher et al.	10/396,975 03-24-2003
	8730	US 6,510,518 B1	01-21-2003	Paul C. Kocher et al.	
	8731	US 6,654,884 B1	11-25-2006	Paul C. Kocher et al.	

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	V56	Grounds Of Opposition, <u>European Patent 1092297</u> in the name of Cryptography Research, Inc., Opposition by Visa Europe Services, Inc., January 25, 2008.			
	V57	Posting on sci.crypt newsgroup, KOCHER, Paul C et al., "Announce: Timing cryptanalysis of RSA, DH, DSS" et al., messages 1-51 of 51, Dec. 11, 1995 through Dec. 24, 1995, <a href="http://groups.google.com/group/sci.crypt">http://groups.google.com/group/sci.crypt</a>			
	V58	DAEMEN, Joan, "Management of Secret Keys: Dynamic Key Handling", Course on Computer Security and Industrial Cryptography (COSIC '97 - June 1997) Presentation Slides; and declaration of Professor Bart Preneel dated 15 June 2007.			
	V59	DAVIES & PRICE, Security for Computer Networks: An Introduction to Data Security in Teleprocessing and Electronic Funds Transfer, 2nd Ed., John Wiley & Sons, New York, NY, 1989, pp. 318-321.			
	V60	PIPER, F., Key Management (Part 3.5) ZERGO: Information Security Training Club, Hampshire, U.K., January 1993, Foils 6-18 to 6-30.			
	V61	PIPER, F., Declaration of, Jan. 21, 2008, University of London, England.			
	V62	BRADLEY, S., "Derived Unique Key Per Transaction Schemes," Some Applications of Mathematics to the Theory of Communications, Ch. 4, pp. 132-199, Ph.D. Thesis, University of London, England, 1994.			
	V63	ISO (International Organization for Standardization), Banking - Key management (retail), "Part 3: Key life cycle for symmetric ciphers", ISO 11568-3 First edition, December 1, 1994, pp. 1-16, <a href="http://www.saiglobal.com/shop">www.saiglobal.com/shop</a>			
	V64	American National Standard for Financial Services, secretariat - American Bankers Association (ANS/ABA X9.24-200x), Key Management Using Triple DEA and PKI, revised by Terry Benson, American National Standards Institute, September 12, 2000.			
	V65	MENZES, A.J. et al., Handbook of Applied Cryptography, pp. 71, 586, 636-637, CRC Press, Boca Raton, Florida (1997).			
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	V66	Interbank Card Association, PIN Manual: A Guide to the Use of Personal Identification Numbers in Interchange, pp. 61- 127, 1979.			
	V67	SEdgeWICK, Robert, Algorithms, 2nd Ed., Chs. 4 and 11, Addison-Wesley, Arlington, VA, 1988.			
	V68	BRASSARD, Gilles, "On computationally secure authentication tags requiring short secret shared keys", <u>Adv. of Crypt.</u> : Proceedings of Crypto-82, D. Chaum, R.L. Rivest, and A.T. Sherman, Eds. Plenum Press, New York, NY, 1982, pp. 79-86.			
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/VH/	V139	US-6,185,596 B1	02-06-2001	Hadad et al.	

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	V47	DEFENDANT VISA INTERNATIONAL SERVICE ASSOCIATION'S FINAL INVALIDITY CONTENTIONS FOR U.S. PATENT 6,539,092, FILED IN CASE C04-4143 JW on November 21, 2007, in US District Court for N. District of California, San Jose Division, including exhibits A through C17.			
	V48	BEKER, H.J. et al., "Simplifying Key Management in Electronic Fund Transfer Point of Sale Systems", <u>Electronics Letters</u> , 19(20):442-444, June 9, 1983.			
	V49	BLUM, L. et al.; "A Simple Unpredictable Pseudo-Random Number Generator", <u>Siam J. Comput.</u> , 13(2):364-383, May 1986.			
	V50	DAEMEN, Joan, "Management of Secret Keys: Dynamic Key Handling", <u>COSIC '97 Course</u> , LNCS 1528, Springer-Verlag, Berlin Heidelberg, Germany, 1998, pp. 262-276.			
	V51	DAEMEN, Joan et al., "The Block Cipher SQUARE" <u>Fast Software Encryption '97</u> , LNCS, 1267, Springer-Verlag, Berlin Heidelberg, Germany, 1997, pp. 1 - 19.			
	V52	ISO (International Organization for Standardization), "Banking - Key management (retail) - Part 2: Key management techniques for symmetric ciphers", ISO 11568-2 First edition 1994-12-01, pp.1-16, <a href="http://www.saiglobal.com/shop">www.saiglobal.com/shop</a>			
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	V70	US 2,733,432 B1	01-31-1956	Breckman	
	V71	US 3,816,762 B1	06-11-1974	Holt Jr.	
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	V73	US 4,563,546 B1	01-07-1986	Glitz	
	V74	US 4,570,084 B1	02-11-1986	Griffin et al.	
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	V92	US 5,737,419 B1	04-07-1998	Ganesan	
	V93	US 5,821,775 B1	10-13-1998	Mehta et al.	

Examiner  
Signature

/Virgil Herring/ (05/15/2008)

Date  
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			Application Number	09/930,836	
			Filing Date	August 15, 2001	
			First Named Inventor	Paul C. Kocher	
			Art Unit	2132	
			Examiner Name	Virgil A. Herring	
Sheet	2	of	6	Attorney Docket Number	44424162-8724

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
	V94	US 5,859,548 B1	01-12-1999	Kong	
	V95	US 5,905,399 B1	05-18-1999	Bosnyak et al.	
	V96	US 5,917,754 B1	06-29-1999	Pathak et al.	
	V97	US 6,046,608 B1	04-04-2000	Theogarajan	
	V98	US 6,066,965 B1	05-23-2000	Blomgren et al.	
	V99	US 6,069,497 B1	05-30-2000	Blomgren et al.	
	V100	US 6, 078,663	06-20-2000	Yamamoto	
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	V103	US 6,181,596 B1	01-30-2001	Horne et al.	
	V104	US 6,211,456 B1	04-03-2001	Seningen et al.	
	V105	US 6,442,525 B1	08-27-2002	Silverbrook et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
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Examiner Signature	/Virgil Herring/ (05/15/2008)	Date Considered	
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				Application Number	09/930,836
				Filing Date	August 15, 2001
				First Named Inventor	Paul C. Kocher
				Group Art Unit	2132
Examiner Name	Virgil A. Herring				
Sheet	3	of	6	Attorney Docket No.	44424162-8724
<b>OTHER ITEMS – NON PATENT LITERATURE DOCUMENTS</b>					
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	V106	DAVIES & PRICE, Security for Computer Networks: An Introduction to Data Security in Teleprocessing and Electronic Funds Transfer, John Wiley & Sons, New York, NY, 2nd Ed., 1989, 377 pages (entire book).			
	V107	Defendant VISA International Service Association's Final Invalidity Contentions for U.S. Patent No. 6,304,658 Pursuant to Patent L.R. 3-6(b), February 8, 2008, Case No. C04-04143 JW(HRL), U.S. District Court, Northern District of California, San Jose Division.			
	V108	Defendant VISA International Service Association's Final Invalidity Contentions for U.S. Patent No. 6,381,699 Pursuant to Patent L.R. 3-6(b), February 8, 2008, Case No. C04-04143 JW(HRL), U.S. District Court, Northern District of California, San Jose Division.			
	V109	Defendant VISA International Service Association's Final Invalidity Contentions for U.S. Patent No. 6,510,518 Pursuant to Patent L.R. 3-6(b), February 8, 2008, Case No. C04-04143 JW(HRL), U.S. District Court, Northern District of California, San Jose Division.			
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	V112	BACK, Adam, "non-interactive forward secrecy", posting to Cypherpunks.Venona.com message board, September 6, 1996. <a href="http://cypherpunks.venona.com/date/1996/09/msg00561.html">http://cypherpunks.venona.com/date/1996/09/msg00561.html</a>			
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				Application Number	09/930,836
				Filing Date	August 15, 2001
				First Named Inventor	Paul C. Kocher
				Group Art Unit	2132
				Examiner Name	Virgil A. Herring
Sheet	4	of	6	Attorney Docket No.	44424162-8724
<b>OTHER ITEMS – NON PATENT LITERATURE DOCUMENTS</b>					
Examiner Initials*	CiteNo. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T <sup>2</sup>
	V114	EBERLE, Hans & THACKER, Charles P., "A 1 GBIT/Second GAAS DES Chip", <u>Proceedings of the 1992 IEEE Custom Integrated Circuits Conference</u> , May 3-6, 1992.			
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	V122	KARLSSON, Magnus et al., "Implementation of bit-serial adders using robust differential logic," <u>Proc. IEEE Nordic Event in ASIC Design Conf., NORCHIP'97</u> , Tallin, Estonia, Nov 10-11, 1997.			
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	V124	LIN, Mao-Chao, "Constant Weight Codes for Correcting Symmetric Errors and Detecting Unidirectional Errors", <u>IEEE Transactions on Computers</u> , 42(11): 1294-1302, November 1993.			
	V125	LOY, James R., "Managing Differential Signal Displacement", thesis, Rensselaer Polytechnic Institute, Troy, New York, August 1993.			
	V126	MAIER, Cliff A. et al., "A 533-MHz BiCMOS Superscalar RISC Microprocessor", <u>IEEE Journal of Solid-State Circuits</u> , 32(11):1625-1634, November 1997.			
	V127	MAIER, Cliff, "High Speed Microprocessor Cache Memory Hierarchies for Yield-Limited Technologies", dissertation, Rensselaer Polytechnic Institute, Troy, New York, August 1996.			
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				First Named Inventor	Paul C. Kocher
				Group Art Unit	2132
Examiner Name	Virgil A. Herring				
Sheet	6	of	6	Attorney Docket No.	44424162-8724
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	V131	SCHETTLER, Helmut et al., "A CMOS Mainframe Processor with 0.5- $\mu$ m Channel Length", <u>IEEE Journal of Solid-State Circuits</u> , 25(5):1166-1177, October 1990.			
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	V133	TABOR, Jeff F., "Noise Reduction Using Low Weight and Constant Weight Coding Techniques", dissertation, Massachusetts Institute of Technology, May 11, 1990, Cambridge Massachusetts.			
	V134	TAKASHIMA, Daisaburo et al, "Noise Suppression Scheme for Gigabit-Scale and Gigabyte/s Data-Rate LSI's", <u>IEEE Journal of Solid-State Circuits</u> , 33(2):260-267, February 1998.			
	V135	TALLINI, Luca G. & BOSE, Bella, "Design of Balanced and Constant Weight Codes for VLSI Systems", <u>IEEE Transactions on Computers</u> , 47(5):556-572, May 1998.			
	V136	TEXAS INSTRUMENTS, SN54LS138, SN54S138, SN74LS138, SN74S138A 3-LINE TO 8-LINE DECODERS/DEMULTIPLEXERS, Texas Instruments, Dallas, Texas, revised March 1988.			
	V137	WANG, L.K. & CHEN, Howard H., "On-Chip Decoupling Capacitor Design to Reduce Switching-Noise-Induced Instability in CMOS/SOI VLSI", <u>Proceedings 1995 IEEE International SOI Conference</u> , pp. 100-101, October 1995.			
	V138	WILLIAMS, Ted E. & HOROWITZ, Mark, "Bipolar Circuit Elements Providing Self-Completion-Indication", <u>IEEE Journal of Solid-State Circuits</u> , 25(1):309-312, February 1990.			
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